

IDS 12/21/2006

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**
 (Not for submission under 37 CFR 1.99)

Application Number	10644850
Filing Date	2003-21-08
First Named Inventor	Mahito Shinohara
Art Unit	2622
Examiner Name	Pritham D. Prabhakher
Attorney Docket Number	03500.017485.

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PP	2	6150676		2000-11-21	Sasaki 257/191	
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FOREIGN PATENT DOCUMENTS

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		Attorney Docket Number		03500.017485.	

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PP	1	European Search Report dated October 30, 2006 for related application no. 03018921.1-1235	<input type="checkbox"/>

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Examiner Signature	/Pritham Prabhakher/	Date Considered	01/24/2007
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			APPLICANT MAHITO SHINOHARA				
			FILING DATE August 21, 2003	GROUP Not Yet Assigned			
U.S. PATENT DOCUMENTS							
*EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
PP		4,962,412	10/09/90	Shinohara et al.	357	30	
PP		5,008,206	04/16/91	Shinohara et al.	437	3	
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		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION YES/NO/ OR ABSTRACT
PP		8-4131	01/17/96	Japan			Abstract and see U.S. Patent No. 4,962,412
OTHER DOCUMENT(S) (Including Author, Title, Date, Pertinent Pages, Etc.)							
PP		<u>A. Yusa et al., "SIT Image Sensor: Design Considerations and Characteristics", IEEE Transactions, vol. ED-33, pp. 735-742, June 1886.</u>					
PP		<u>N. Tanaka et al., "A 310K Pixel Bipolar Imager (BASIS)", IEEE Transactions Electron Devices, vol 37, pp 964-97, No. 4, April 1990.</u>					
PP		<u>S. Mendis et al., "A 128 x 128 CMOS Active Pixel Image Sensor for Highly Integrated Imaging Systems", IEDM Tech. Digest, 1993, pp. 583-586.</u>					
PP		<u>Nakamura et al., "Gate Accumulation Type MOS Photo Transistor Image Sensor", National Conference of Television Society, 3-7, 1986.</u>					
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